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STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: Martin Heinen, et al.	
Application No./Patent No.: 10557104 Filed/Issue Date: 2006-11-27	
Entitled: Integrated Circuit With Bit Error Test Capability	
Agilent Technologies, Inc. , a Corporation (Type of Assignee, e.g., corporation, partnership, university, government age	ency, etc.)
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2. an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)	
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The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	
Signature Date	
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Printed or Typed Name Telephone Num	ber
Vice President, Deputy General Counsel, Director Intellectual Property Title	

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